

NEXTIO SELECTS DENALI PURESPEC FOR PCI VERIFICATION.: An Article From: Electro Manufacturing [HTML] [Digital]

Denali Announces a Major Cost Reduction Plan. - -

Oct 10, 1999 Denali Announces a Major Cost Reduction PCI Express interface development. Denali's PureSpec used by Agere for PCI Express interface verification.

<http://www.thefreelibrary.com/Denali+Announces+a+Major+Cost+Reduction+Plan.-a056187076>

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Patent US8090797 - Methods and systems for -

a SONET (Synchronous Optical Network) network; a SDH (Synchronous Digital bus; a PCI bus; a PCI-X bus; a PCI-Express more farm-side interface(s)

<http://www.google.com.jm/patents/US8090797>

Denali Databahn DRAM DRAM Memory Controller IP -

Denali s unique Databahn physical interface minimizes expensive, PureSpec PCI Express is the industry leading solution for verifying compliance

http://www.newplus.com.cn/products/denali/Datasheet_DRAM_5.pdf

PCI Express switch. - Free Online Library -

Oct 31, 2005 PCI Express switch. Unisys selects Denali for PCI Express interface development. NEXTIO SELECTS DENALI PURESPEC FOR PCI VERIFICATION.

<http://www.thefreelibrary.com/PCI+Express+switch.-a0142433469>

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<http://www.amazon.com/NEXTIO-SELECTS-DENALI-PURESPEC-VERIFICATION/dp/B000B7056C>

Computer interfacing - PCI Express -

PCI Express Interface Directory: Unisys Selects Denali for PCI Express Unisys engineers are using Denali s PureSpec verification IP to model,

<http://www.thaiio.com/interface-catalog/pci-express-interface/>

Denali s PureSpec used by Agere for PCI Express -

Denali s PureSpec used by Agere for PCI Express interface verification.: An article from: Software Industry Report

<http://www.ulearnprogramming.net/product/denalis-purespec-used-by-agere-for-pci-express-interface-verification-an-article-from-software-industry-report/>

Cadence and Denali Partnership -

The flow is centered around Denali PureSpec verification within the Cadence Incisive Verification Platform. PureSpec is a

http://www.cadence.com/alliances/ip_program/pages/denali.aspx

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Unisys selects Denali for PCI Express interface -

Dec 14, 2003 Free Online Library: Unisys selects Denali for PCI Express interface development. by "EDP Weekly's IT Monitor"; Business Computers and office automation

<http://www.thefreelibrary.com/Unisys+selects+Denali+for+PCI+Express+interface+development.-a0111360198>

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The flow is centered around Denali PureSpec verification within the Cadence Incisive Verification Platform. PureSpec is a http://www.cadence.com/alliances/ip_program/pages/denali.aspx

Denali Announces PureSpec Solution Featured in -

(EDA) software and intellectual property (IP), today announced Denali's PureSpec(TM) verification IP support for IBM's PowerPC(R) processor local bus

http://www.redorbit.com/news/technology/1754381/denali_announces_purespec_solution_featured_in_ibms_new_powerpc_plb6/

Denali PureSpec SATA PureSpec SATA -

than 400 companies worldwide use Denali's tools, technology, www.denali.com/purespec Contact Information North American Headquarters Denali Software, Inc.

http://www.newplus.com.cn/products/denali/Datasheet_purespec_sata_9.pdf

PCI bridge | Article about PCI bridge by The Free -

Explanation of PCI bridge. PCI bridge Denali's PureSpec used by Agere for PCI Express interface verification by EDP Weekly's IT Monitor.

<http://encyclopedia2.thefreedictionary.com/PCI+bridge>

Denali Launches Blueprint, New Software for -

Denali Launches Blueprint, New Software for System-on-Chip Design. Denali's PureSpec Verification IP product supports all complex interfaces,

<http://www.chipdesignmag.com/print.php?articleId=142?issueId=0>

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<http://paymentsmarket.com/search>

Denali Releases SystemRDL Language Specification -

Denali Releases SystemRDL Language Specification. Denali's Databahn Databahn, Dataplex, MMAV, PureSpec, Spectra,

<http://edageek.com/2006/05/30/denali-releases-systemrdl-language-specification/>

Denali Memory Report Serial Interfaces - -

Denali s PureSpec Veri cation IP product supports all complex interfaces, including PCI Express, Advanced Switching Interconnect (ASI), USB, and SATA II.

<http://discobolusdesigns.com/personal/DMR-Jan05.pdf>

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