

Heterojunction Bipolar Transistors For Circuit Design: Microwave Modeling And Parameter Extraction By Jianjun Gao

By Jianjun Gao

A Nonlinear Circuit Simulation Model for GaAs and -

Over the last several years, III-V heterojunction bipolar transistors (HBT) have become an increasingly important technology for a variety of rapidly growing markets.

<http://www.microwavejournal.com/articles/3797-a-nonlinear-circuit-simulation-model-for-gaas-and-inp-heterojunction-bipolar-transistors?v=preview>

Amazon.com: transistor equivalent -

Heterojunction Bipolar Transistors for Circuit Design: Microwave Modeling and Parameter Extraction Apr 27, 2015. by Jianjun Gao. Heterojunction Bipolar

<http://www.amazon.com/s?ie=UTF8&page=1&rh=i%3Aaps%2Ck%3Atransistor%20equivalent>

Double heterojunction bipolar transistors - IEEE -

Double heterojunction bipolar transistors Information on IEEE's A 12 Gb/s laser and optical modulator driver circuit with InGaAs/InP double heterostructure

<http://technav.ieee.org/tag/5626/double-heterojunction-bipolar-transistor>

MEXTRAM model based SiGe HBT large-signal modeling -

Cheng Jiali, Yin Qiuyan and Gao Jianjun. Geelen J A M Parameter extraction for the bipolar based compact bipolar transistor model for circuit design

<http://iopscience.iop.org/1674-4926/31/10/104004/refs>

Direct extraction of equivalent circuit parameters -

A new method is presented for the direct extraction of hybrid-T equivalent circuits for heterojunction bipolar transistors. The method differs from previous ones by

<http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=41453>

History Of The Heterojunction Bipolar Transistor -

Heterojunction bipolar transistors are bipolar junction transistors, A typical digital CMOS circuit, there are two main classes of power dissipation,

<http://www.ukessays.com/essays/engineering/history-of-the-heterojunction-bipolar-transistor-engineering-essay.php>

TransferredSubstrate Heterojunction Bipolar -

TransferredSubstrate Heterojunction Bipolar Transistor Integrated Circuit Technology - PowerPoint PPT Presentation

http://www.powershow.com/view1/26ffc0-ZDc1Z/TransferredSubstrate_Heterojunction_Bipolar_Transistor_Integrated_Circuit_Technology_powerpoint_ppt_presentation

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<http://www.shop.com/search/transistors>

Transistors - ABE-IPS -

Heterojunction Bipolar Transistors for Circuit Design: Microwave Modeling and Parameter Extraction. Jianjun Gao. Operation and Modeling of the MOS Transistor 3e.

<http://www.abe.pl/en/c/1796/transistors>

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<http://www.e-bookdownload.net/search/heterojunction-bipolar-transistors-for-circuit-design>

RF circuit design: theory and applications - Data -

Pavel Bretschko, "RF Circuit Design: Heterojunction Bipolar Transistors for Circuit Design: Microwave Modeling and Parameter Extraction by Jianjun Gao

<http://avxsearch.se/?q=RF%20circuit%20design:%20theory%20and%20applications>

PPT Heterojunction Bipolar Transistors -

4 HETEROJUNCTION BIPOLEAR TRANSISTORS. The major development in bipolar transistors (since 1990) HBTs break the link between NB and ? Do this by making different

http://www.powershow.com/view1/d73e1-ZDc1Z/Heterojunction_Bipolar_Transistors_powerpoint_ppt_presentation

Small-Signal Modeling and Parameter Extraction of -

Heterojunction Bipolar Transistors for Circuit Design: Microwave Modeling and Parameter Extraction. Microwave Modeling and Parameter Extraction,

<http://onlinelibrary.wiley.com/doi/10.1002/9781118921531.ch5/references>

Patents - Google Books -

and heterojunction bipolar transistors for amplifying Patents Publication bipolar transistor and integrated circuit device using

<http://www.google.com/patents/US6727530>

Optoelectronic integrated circuit design and -

Optoelectronic integrated circuit design and device modeling. [Jianjun Gao] Microwave Transistors. 5.2 FET Modeling Optoelectronic integrated circuit

<http://www.worldcat.org/title/optoelectronic-integrated-circuit-design-and-device-modeling/oclc/751836028>

Bipolar junction transistor - Wikipedia, the free encyclopedia -

2.3 Heterojunction bipolar transistor; In general, transistor level circuit design is performed using SPICE or a comparable analog circuit simulator,

http://en.wikipedia.org/wiki/Bipolar_junction_transistor

Electronics engineering Readings.com.au -

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<http://www.readings.com.au/books/category/1792-electronics-engineering>

High-frequency noise in heterojunction bipolar -

Heterojunction bipolar transistors The equivalent circuit of the intrinsic device depicted in Fig. 1 is used to describe the noise behaviour of an HBT in the

<http://www.sciencedirect.com/science/article/pii/S0038110197002906>

Ultra-wideband tunable circuit design using -

Ultra-wideband tunable circuit design using silicon-germanium heterojunction bipolar transistors

<http://smartech.gatech.edu/handle/1853/34807>

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