

Heterojunction Bipolar Transistors For Circuit Design: Microwave Modeling And Parameter Extraction By Jianjun Gao

By Jianjun Gao

A method to simultaneously extract the -

A method to simultaneously extract the small-signal equivalent circuit and noise parameters of heterojunction bipolar transistors

<http://onlinelibrary.wiley.com/doi/10.1002/mop.21649/abstract>

A Nonlinear Circuit Simulation Model for GaAs and -

Over the last several years, III-V heterojunction bipolar transistors (HBT) have become an increasingly important technology for a variety of rapidly growing markets.

<http://www.microwavejournal.com/articles/3797-a-nonlinear-circuit-simulation-model-for-gaas-and-inp-heterojunction-bipolar-transistors?v=preview>

PPT Heterojunction Bipolar Transistors -

4 HETEROJUNCTION BIPOLAR TRANSISTORS. The major development in bipolar transistors (since 1990) HBTs break the link between NB and ? Do this by making different

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High-frequency noise in heterojunction bipolar -

Heterojunction bipolar transistors The equivalent circuit of the intrinsic device depicted in Fig. 1 is used to describe the noise behaviour of an HBT in the

<http://www.sciencedirect.com/science/article/pii/S0038110197002906>

Small-Signal Modeling and Parameter Extraction of -

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<http://www.readings.com.au/books/category/1792-electronics-engineering>

Amazon.com: transistor equivalent -

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<http://www.amazon.com/s?ie=UTF8&page=1&rh=i%3Aaps%2Ck%3Atransistor%20equivalent>

SiGe Heterojunction Bipolar Transistors: Peter -

SiGe Heterojunction Bipolar Transistors is an essential tool for practising process engineers and integrated circuit designers in the semiconductor,

<http://www.amazon.com/Heterojunction-Bipolar-Transistors-Peter-Ashburn/dp/0470848383>

Optoelectronic integrated circuit design and -

Optoelectronic integrated circuit design and device modeling.

[Jianjun Gao] Microwave Transistors. 5.2 FET Modeling

Optoelectronic integrated circuit

<http://www.worldcat.org/title/optoelectronic-integrated-circuit->

[design-and-device-modeling/oclc/751836028](http://www.tandfonline.com/doi/full/10.1080/00207217.2011.567037)

MEXTRAM model based SiGe HBT large-signal modeling -

Cheng Jiali, Yin Qiuyan and Gao Jianjun. Geelen J A M Parameter extraction for the bipolar based compact bipolar transistor model for circuit design

<http://iopscience.iop.org/1674-4926/31/10/104004/refs>

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http://scholarworks.umass.edu/cgi/viewcontent.cgi?article=1125&context=masters_theses_2

An improved small-signal model for SiGe HBTs - -

Contents > An improved small-signal model for SiGe bipolar transistor, parameter extraction, Circuit Design and Device Modeling,

<http://www.tandfonline.com/doi/full/10.1080/00207217.2011.567037>

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Heterojunction bipolar transistor - Wikipedia, -

The heterojunction bipolar transistor (HBT) is a type of bipolar junction transistor (BJT) which uses differing semiconductor materials for the emitter and base

http://en.wikipedia.org/wiki/Heterojunction_bipolar_transistor

History Of The Heterojunction Bipolar Transistor -

Heterojunction bipolar transistors are bipolar junction transistors, A typical digital CMOS circuit, there are two main classes of power dissipation,

<http://www.ukessays.com/essays/engineering/history-of-the-heterojunction-bipolar-transistor-engineering-essay.php>

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