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Chris Freymuth | LinkedIn -

I have been a VLSI design engineer since I artwork generation and test/debug of chips. Specialties:VLSI circuit Microprocessor design engineer Advanced Micro

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http://apt.cs.manchester.ac.uk/ftp/pub/apt/papers/BIST_mpipe.iece.pdf

A proposed hardware fault simulation engine -

G. Russell. Fault simulation E. Lindbloom, and T. McCarthy. Fault simulation for structured VLSI. Behavioral-Level Fault Simulation, IEEE Design & Test, v.5

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G. Russell, Advanced Simulation and Testing Methodologies for VLSI Design, Van Nostrand, London, 1989.

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