

# Advanced Simulation And Test Methodologies For VLSI Design By G. Russell

By G. Russell

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**Dr Gordon Russell - Newcastle University -**

Investigator on an SERC funded Project into VLSI Design Methodologies, TEST Project Leader(s): Dr Gordon Russell; Russell, G. Gate Level Simulation

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G. Russell, Advanced Simulation and Testing Methodologies for VLSI Design, Van Nostrand, London, 1989.

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### **Chris Freymuth | LinkedIn -**

I have been a VLSI design engineer since I artwork generation and test/debug of chips. Specialties:VLSI circuit Microprocessor design engineer Advanced Micro

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